IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

SHI-QING WANG, ET AL

Docket: 30-4687 (4780)

Serial Number: 09/328,646

Group Art Unit: 2811

Filed: June 9, 1999

Examiner: H. Vu

For: INTEGRATED CIRCUITS WITH MULTIPLE LOW DIELECTRIC-

CONSTANT INTER- METAL DIELECTRICS

NOTICE: THIS APPLICATION IS UNDER FINAL REJECTION

FAX COVER SHEET

TO:

Commissioner for Patents

Washington, D.C. 20231

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EXAMINER: H. Vu GROUP: 2811

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<u>AMENDMENT</u>

Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action mailed January 30, 2002, please amend the above identified patent application as follows:

In the claims:

CLEAN COPY OF AMENDED CLAIMS

23. (Twice Amended) An integrated circuit structure which comprises a substrate and